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(11) **EP 1 162 726 A1**

(12)

EUROPEAN PATENT APPLICATION
published in accordance with Art. 158(3) EPC

(43) Date of publication:

12.12.2001 Bulletin 2001/50

(51) Int Cl.7: **H02P 3/22**

(21) Application number: **00908038.3**

(86) International application number:

PCT/JP00/01512

(22) Date of filing: **13.03.2000**

(87) International publication number:

WO 00/55963 (21.09.2000 Gazette 2000/38)

(84) Designated Contracting States:

**AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE**

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(30) Priority: **18.03.1999 JP 7415999**

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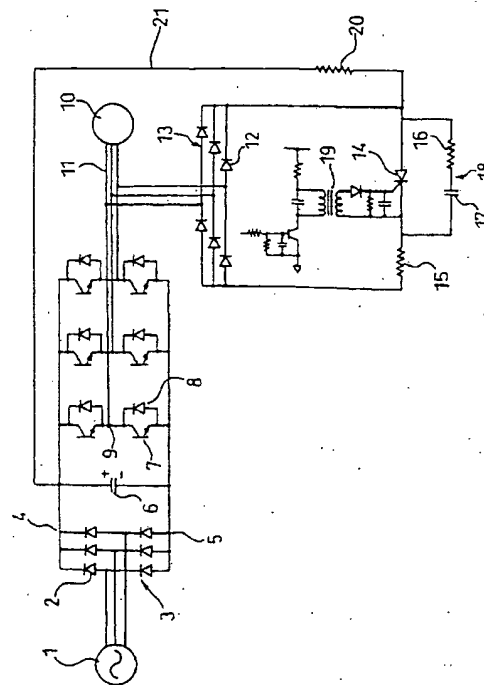
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(54) **DYNAMIC BRAKE CIRCUIT AND SEMICONDUCTOR INVERTER USING DYNAMIC BRAKE CIRCUIT**

(57) The invention prevents a voltage change dv/dt of an inverter from directly causing an erroneous firing of a semiconductor brake switch, which may short-circuit the inverter during operation. To brake a motor (10) driven by an inverter which consists of a three-phase AC power supply (1), a three-phase bridge rectifier (3), a smoothing capacitor (6) and a semiconductor switching element (7), a dynamic braking circuit turns on a semiconductor braking switch (14) to short-circuit feeder lines (11) for the motor (10) so that a braking resistor (15) may absorb energy. A large resistor (20) is connected between the positive side of the smoothing capacitor (6) and the positive side of the semiconductor braking switch (14), and a snubber capacitor (17) of a snubber circuit (18) connected in parallel with the semiconductor braking switch (14) is charged through the resistor (20) before the inverter is started.

Fig. 1



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Description

Technical Field

[0001] The present invention relates to prevention of erroneous operations of a dynamic brake circuit of a motor to be driven by an inverter comprised of semiconductor switching devices.

Background Art

[0002] To stop inverter drive motors, which are represented by permanent magnet synchronous motors, in a case of emergency, a method called "dynamic brake" has been employed in which the operation of the inverter is stopped, power generated from the power supply lines of the motor that rotates under inertia is short-circuited with a brake resistor and consumed as heat energy, and the energy is absorbed for braking.

[0003] Fig. 5 is a circuit diagram of a conventional brake for an inverter drive motor.

[0004] In this figure, to anode 26 at the output of the bridge circuit having six diodes 25 connected to a three-phase AC power supply, one end of smoothing resistor 27 is connected, and smoothing capacitor 29 is connected between the other end of this smoothing resistor 27 and cathode 28 at the output of the bridge circuit. In parallel with this smoothing capacitor 29, three pairs of semiconductor switching devices 30 such as transistors and thyristors, each two of which are connected together in series, are connected. The intermediate connecting points of these semiconductor switching devices 30 are connected to the power supply lines 32 of the motor 31, and the motor is driven at a phase corresponding to the switching timing of the semiconductor switching devices 30.

[0005] On the other hand, to brake this motor 31, a series circuit is provided, which consists of a three-phase bridge rectifier circuit including diodes 33 connected to the power supply lines 32, thyristor 34 as a semiconductor braking switch, and brake resistor 35. A snubber circuit in which snubber resistor 36 and snubber capacitor 37 are connected in series is connected to this thyristor 34 in parallel. In the figure, ignition current limiting resistor 38, photocoupler 39 consisting of a photodiode and a photothyristor, bias capacitor 40, and bias resistor 41 comprise an ignition control circuit of the thyristor 34.

[0006] In such a conventional inverter, in order to cause the motor 31 to make an emergency stop, for example, when continuity of the semiconductor switching devices 30 is interrupted to stop power supply to the motor 31, and the photodiode of the photocoupler 39 is caused to emit light and the photothyristor is caused to electric-insulatingly ignite, a signal is applied to the gate of the thyristor 34, so that the thyristor 34 ignites and makes continuity. Thereby, electricity that has been generated from the motor 31 and sent to the power supply

lines 32 flows to the diodes 33, thyristor 34, brake resistor 35, diodes 33, and power supply lines 32, and then heat is generated and absorbed by the brake resistor 35. Thereby, the motor 31 is rapidly braked.

[0007] However, in a conventional inverter, in the switching operations of the semiconductor switching devices 30 when operating the inverter, if the rate of voltage change dv/dt is excessively great, the critical OFF voltage rise rate of the thyristor is exceeded, the photothyristor and thyristor 34 of the photocoupler 39 are caused to erroneously ignite, and the inverter outputs, that is, the power supply lines 32 are short-circuited although there is no request for braking.

[0008] In order to prevent this problem, the capacitance of the snubber circuit must be made sufficient to suppress the rate of voltage change dv/dt , or a semiconductor braking switch with a sufficiently great critical OFF voltage rise rate must be selected. Therefore, the circuit becomes large and complicated, and cost for the parts increases.

[0009] Therefore, the object of the invention is to provide a safe brake for an inverter drive motor in which the rate of voltage change dv/dt generated by the operation of the semiconductor switching devices of the inverter is not directly applied to the semiconductor braking switch.

Disclosure of Invention

[0010] In order to achieve the abovementioned object, according to the first aspect of the invention, a dynamic brake circuit, which comprises a series circuit including resistors for converting loaded electric energy into heat and semiconductor switching devices, which are connected in series, and a snubber circuit including a capacitor connected in parallel to the semiconductor switching devices, further comprises a charging circuit for charging electricity in the capacitor prior to driving to load.

[0011] Furthermore, according to the second aspect of the invention, in a semiconductor inverter, which comprises a first rectifier for rectifying alternating currents, a smoother for smoothing the output of the first rectifier, and an inverter part for switching the output from the smoother at a desired timing by the first semiconductor switching device, and further comprises a dynamic brake circuit consisting of a second rectifier for rectifying the output from the inverter part and a series circuit including the first resistance and second semiconductor switching device that are connected between the output terminals of the second rectifier, and a snubber circuit including a capacitor connected in parallel to the second semiconductor switching device, the inverter is provided with a charging circuit for charging electricity in the capacitor before the inverter starts an inverter operation.

[0012] Furthermore, according to the third aspect of the invention, the charging circuit is comprised of a second resistance connected between the anode side of

the smoother and anode side of the dynamic brake circuit, and a third switching device.

[0013] Moreover, according to the fourth aspect of the invention, in place of the third switching device, a part of the first semiconductor switching devices is commonly used.

[0014] According to the fifth aspect of the invention, in the inverter according to the second aspect of the invention, which has flywheel diodes connected in antiparallel to the first semiconductor switching device, the flywheel diodes are commonly used in place of the diodes comprising one arm of the second rectifier.

Brief Description of Drawings

[0015] Fig. 1 is a circuit diagram showing an embodiment of a semiconductor inverter with a dynamic brake circuit according to the invention; Fig. 2 is a drawing showing a semiconductor inverter having a thyristor ignition circuit that is different from Fig. 1; Fig. 3 is a circuit diagram showing a modified example of Fig. 1; Fig. 4 is a circuit diagram showing a modified example of Fig. 2; and Fig. 5 is a circuit diagram of a conventional inverter.

Best Mode for Carrying Out the Invention

[0016] Next, an embodiment of the invention is explained in detail with reference to Fig. 1 through Fig. 4.

[0017] Fig. 1 through Fig. 4 are circuit diagrams showing an example in which a charging circuit for charging snubber capacitor 17 of snubber circuit 18.

[0018] In Fig. 1, the anode and cathode of smoothing capacitor 6 are connected to anode 4 and cathode 5 at the output of bridge circuit 3 in which six diodes 2 are connected to three-phase AC power supply 1. Three pairs of semiconductor switching devices 7 such as thyristors, transistors or the like, each two of which are connected in series, are connected in parallel to the smoothing capacitor 6 to form an inverter part. Flywheel diodes 8 are connected in antiparallel to the semiconductor switching devices 7, and the intermediate connecting points 9 of the semiconductor switching devices 7 are formed as output points of each phase, and to these points, power supply lines 11 of each phase of motor 10 are connected. The phase of the motor 10 is controlled with characteristics corresponding to the switching timings of the semiconductor switching devices 7.

[0019] In order to brake this motor 10, a dynamic brake circuit is provided in which three-phase bridge circuit 13 including diodes 12 connected to the power supply lines 11, thyristor 14 as a semiconductor braking switch, and brake resistor 15 are connected in series.

[0020] Furthermore, snubber circuit 18 including snubber resistor 16 and snubber capacitor 17 connected in series is connected in parallel to the thyristor 14. To the gate electrode of the thyristor 14, an ignition circuit using a pulse transformer is connected.

[0021] Charging circuit 21 comprised of charging re-

sistor 20 with a high ohmic value is connected between the anode side of the thyristor 14 and the anode side of the smoothing capacitor 6. In the abovementioned circuitry, when the snubber capacitor 17 is charged prior to driving the inverter part, any of the semiconductor switching devices 7 of the negative side arm of the inverter part may be ignited. Then, a closed charging circuit is formed from the anode of the smoothing capacitor 6, charging resistor 20 of the charging circuit 21, snubber resistor 16 of the snubber circuit 18, snubber capacitor 17, brake resistor 15, the negative side arm diode of the three-phase bridge circuit 13, power supply lines 11, semiconductor switching devices 7 to the cathode of the smoothing capacitor 6, and charges in the smoothing capacitor 6 are charged in the snubber capacitor 17.

[0022] The voltage at both ends of the snubber capacitor 17, that is, the voltage at both ends of the thyristor 14 after charging is finished becomes equal to the voltage of the smoothing capacitor 6.

[0023] The charging time constant $\tau(t)$ in this case is expressed by the following equation (1).

$$\tau(t) = (R15+R16+R20) \times C17 \quad (1)$$

[0024] Thus, according to the invention, the snubber capacitor 17 is charged in the condition where an extreme rate of voltage change dv/dt is suppressed by the charging resistor 20 with a high ohmic value and the time constant of the snubber capacitor 17, and even if the inverter operation is started after the snubber capacitor 17 is charged, since the same voltage as that of the smoothing capacitor 6 as an input of the inverter part has already been charged in the snubber capacitor 17, dv/dt due to switching of the transistors comprising the inverter part is not applied at all to the thyristor 14 that functions as a switch of the dynamic brake circuit, and therefore, erroneous ignition of the thyristor 14 is prevented.

[0025] In addition, it becomes unnecessary to make the thyristor 14 for prevention of erroneous ignition sufficiently resistant against dv/dt , and also, the snubber circuit 18 can be simplified to be small in capacitance.

[0026] As switching devices for the dynamic brake circuit, thyristors are used in this embodiment, however, they are not limited to thyristors, but may be mechanical switches (for example, relays) as well as triacs, transistors, or the like. As a method for driving these semiconductor switching devices, currents are flown into a pulse transformer or relay coil as well as a photocoupler.

[0027] The resistors provided in the charging circuit may be disposed at any locations within the circuit, and if the charging period of time is adjustable by control of the current by means of the charging means, the resistors are not necessary.

[0028] In addition, in the embodiment of the invention, the transistor (first semiconductor switching devices) of

the inverter part is commonly used as a charging circuit opening and closing switch, however, as a matter of course, a third switching device exclusively for opening and closing the charging circuit may be externally and separately provided.

[0029] Fig. 2 shows an example of a case where the ignition circuit of the example shown in Fig. 1 is changed.

[0030] In Fig. 2, the anode and cathode of the smoothing capacitor 6 are connected to the anode 4 and cathode 5 at the outputs of the bridge circuit in which six diodes 2 are connected to the three-phase AC power supply 1. Three pairs of semiconductor switching devices 7, each two of which are connected in series, are connected in parallel to the smoothing capacitor 6 to form an inverter part. Flywheel diodes 8 are connected to the semiconductor switching devices 7, respectively, and the intermediate connecting points 9 of the semiconductor switching devices 7 are formed as output points of each phase, and to the points, the power supply lines 11 of each phase of the motor 10 are connected. The phase of the motor 10 is controlled by characteristics corresponding to the switching timings of the semiconductor switching devices 7.

[0031] In order to brake this motor 10, a dynamic brake circuit is provided, in which three-phase bridge circuit 13 having diodes 12 connected to the power supply lines 11, thyristor 14, and brake resistor 15 are connected in series. Snubber circuit 18, in which snubber resistor 16 and snubber capacitor 17 are connected in series, is connected in parallel to the thyristor 14. An ignition circuit using photocoupler 22 consisting of a pair of phototransistor and photothyristor is connected to the gate electrode of the thyristor 14. They photocoupler 22 is employed for electric insulation upon replacing the signals at the ignition power supply side with a low voltage and at the thyristor side with a high voltage by light.

[0032] Charging circuit 21 comprised of charging resistor 20 with a high ohmic value relating to the invention is connected between the anode side of the thyristor 14 and the anode side of the smoothing capacitor 6. Thereby, the snubber capacitor 17 is always charged in advance of operation of the inverter part.

[0033] In the abovementioned circuitry, in the case where the snubber capacitor 17 is charged prior to operation of the inverter part, any of the semiconductor switching devices 7 of the negative side arm of the inverter part may be ignited. Then, a closed charging circuit is formed from the anode of the smoothing capacitor 6, charging resistor 20 of the charging circuit 21, snubber resistor 16 of the snubber circuit 18, snubber capacitor 17, brake resistor 15, negative side arm diode of the three-phase bridge circuit 13, power supply lines 11, and semiconductor switching devices 7 to the cathode of the smoothing capacitor 6, whereby charges in the smoothing capacitor 6 are charged in the snubber capacitor 17.

[0034] The voltage at both ends of the snubber capacitor 17, that is, the voltage at both ends of the thy-

ristor 14 after charging is finished becomes equal to the voltage of the smoothing capacitor 6.

[0035] According to the invention thus composed, the snubber capacitor 17 is charged in a condition where an extreme rate of voltage change dv/dt is suppressed by the charging resistor 20 with a high ohmic value and the time constant of the snubber capacitor 17, and even when the inverter operation is started after the snubber capacitor 17 is charged, since the same voltage as that of the smoothing capacitor 6 as the input of the inverter part has already been charged in the snubber capacitor 17, dv/dt due to switching of the transistors comprising the inverter part is not applied at all to the thyristor 14, which functions as a switch for the dynamic brake circuit, and the photothyristor 22 at all. Therefore, erroneous ignition of the thyristor 14 and photothyristor 22 is prevented. In addition, it becomes unnecessary to make the thyristor 14 for prevention of erroneous ignition and photothyristor 22 sufficiently resistant against dv/dt , and also, the snubber circuit 18 can be simplified to be small in capacitance.

[0036] Fig. 3 shows a circuit constructed so that devices of the generation brake circuit and devices of the energy absorbing circuit of the motor power supply lines are commonly used.

[0037] In Fig. 3, the anode and cathode of the smoothing capacitor 6 are connected to the anode 4 and cathode 5 at the output of the bridge circuit 3 in which six diodes 2 are connected to the three-phase AC power supply 1, three pairs of semiconductor switching devices 7 such as transistors, each two of which are connected in series, are connected in parallel to the smoothing capacitor 6 to form an inverter part, flywheel diodes 8 are connected in antiparallel to the semiconductor switching devices 7; the intermediate connecting points 9 of the semiconductor switching devices 7 are formed as output points of each phase, and the points to which power supply lines 11 of each phase of the motor 10 are connected are the same in the case of Fig. 1 and Fig. 2.

[0038] In this example, charging resistor 20 one end of which is connected to the anode side of the smoothing capacitor 6, thyristor 14 anode side of which is connected to the other end of the charging resistor 20, brake resistor 15 one end of which is connected to the cathode side of the thyristor 14, and three diodes 23, anode sides of which are connected to the other end of the brake resistor 15 and cathode sides of which are connected to the power supply lines 11 of each phase of the motor, are provided, and an ignition circuit using pulse transformer 19 is connected to the gate electrode of the thyristor 14.

[0039] In this circuit, in a case where it is necessary to brake the motor 10, when the ignition circuit of the thyristor 14 is turned ON after ignition of the semiconductor switching devices 7 of the inverter part is canceled and the inverter operation is suspended, thyristor 14 conducts electricity. Therefore, a circuit is formed from the power supply lines 11, intermediate connecting

points 9, flywheel diodes 8, charging resistor 20, thyristor 14, brake resistor 15, and diodes 23 to the power supply lines 11, and the brake resistor 15 generates heat and absorbs energy, whereby the motor 10 is braked.

[0040] Thus, according to the present embodiment, the diode group 12 of the anode side arm of the second rectifier 13 of Fig. 1 is replaced with the flywheel diodes 8 of the first semiconductor switching devices 7 of the anode side arm, and this contributes to reduction in size and cost.

[0041] Also, in this circuit, as described above, the snubber capacitor 17 is always charged via the charging resistor 20 to suppress dv/dt of the inverter part, and the diodes 23 prevent direct entrance of dv/dt from the inverter part, so that short-circuiting failures due to the dynamic brake circuit do not occur during the inverter operation.

[0042] Fig. 4 shows an example in which photocoupler 22 comprised of a photodiode and a photothyristor is used for the ignition circuit of the thyristor 14 of the circuit of Fig. 3 in place of the circuit using the pulse transformer 19, wherein other points in construction are the same, so that explanation for the points is omitted by attaching the same symbols as those of Fig. 3 to the same points.

[0043] As described above, according to the invention, in a brake constructed so that, when braking an inverter drive motor, which is driven by switching outputs of a current obtained through rectification of an alternating current and smoothing by a smoothing capacitor, a semiconductor braking switch is ignited, the power supply lines of the motor are short-circuited, and energy is absorbed by a brake resistor, a charging circuit for always charging a snubber capacitor is provided between the anode side of the smoothing capacitor and the anode side of the semiconductor braking switch, whereby the voltage change dv/dt of the inverter part is not applied to the semiconductor braking switch, so that erroneous operations of the semiconductor braking switch can be prevented.

[0044] Therefore, the snubber circuit can be simplified, and a part with low resistance against dv/dt can be used, so that the price of the circuit can be reduced.

Industrial Applicability

[0045] The invention provides a simplified dynamic brake circuit with high reliability at a low price, which can be used for purposes that cause motors to make urgent acceleration and deceleration and emergency stops by using inverters.

Claims

1. A dynamic brake circuit comprising a series circuit consisting of resistors for converting loaded electric energy into heat and semiconductor switching de-

vices, and a snubber circuit including a capacitor connected in parallel to the semiconductor switching devices, wherein

a charging circuit for charging the capacitor prior to drive of the load is provided.

2. A semiconductor inverter comprising a first rectifier for rectifying alternating currents, a smoother for smoothing the output of the first rectifier, an inverter part for switching the output of the smoother by first semiconductor switching devices at desired timings, and further comprising a dynamic brake circuit, which consists of a second rectifier for rectifying the output of the inverter part and a series circuit including a first resistor connected between the output terminals of the second rectifier and a second semiconductor switching device, and a snubber circuit including a capacitor connected in parallel to the second semiconductor switching device, wherein

a charging circuit for charging the capacitor before the inverter part starts inverter operation is provided.

3. A semiconductor inverter according to Claim 2, wherein the charging circuit is comprised of a second resistor and a third switching device that are connected between the anode side of the smoother and the anode side of the dynamic brake circuit.
4. A semiconductor inverter according to Claim 3, wherein a part of the first semiconductor switching devices is commonly used in place of the third switching device.
5. A semiconductor inverter according to Claim 2 comprising flywheel diodes connected in antiparallel to the first semiconductor switching devices, wherein in place of diodes comprising one arm of the second rectifier, the flywheel diodes are commonly used.

Fig. 1

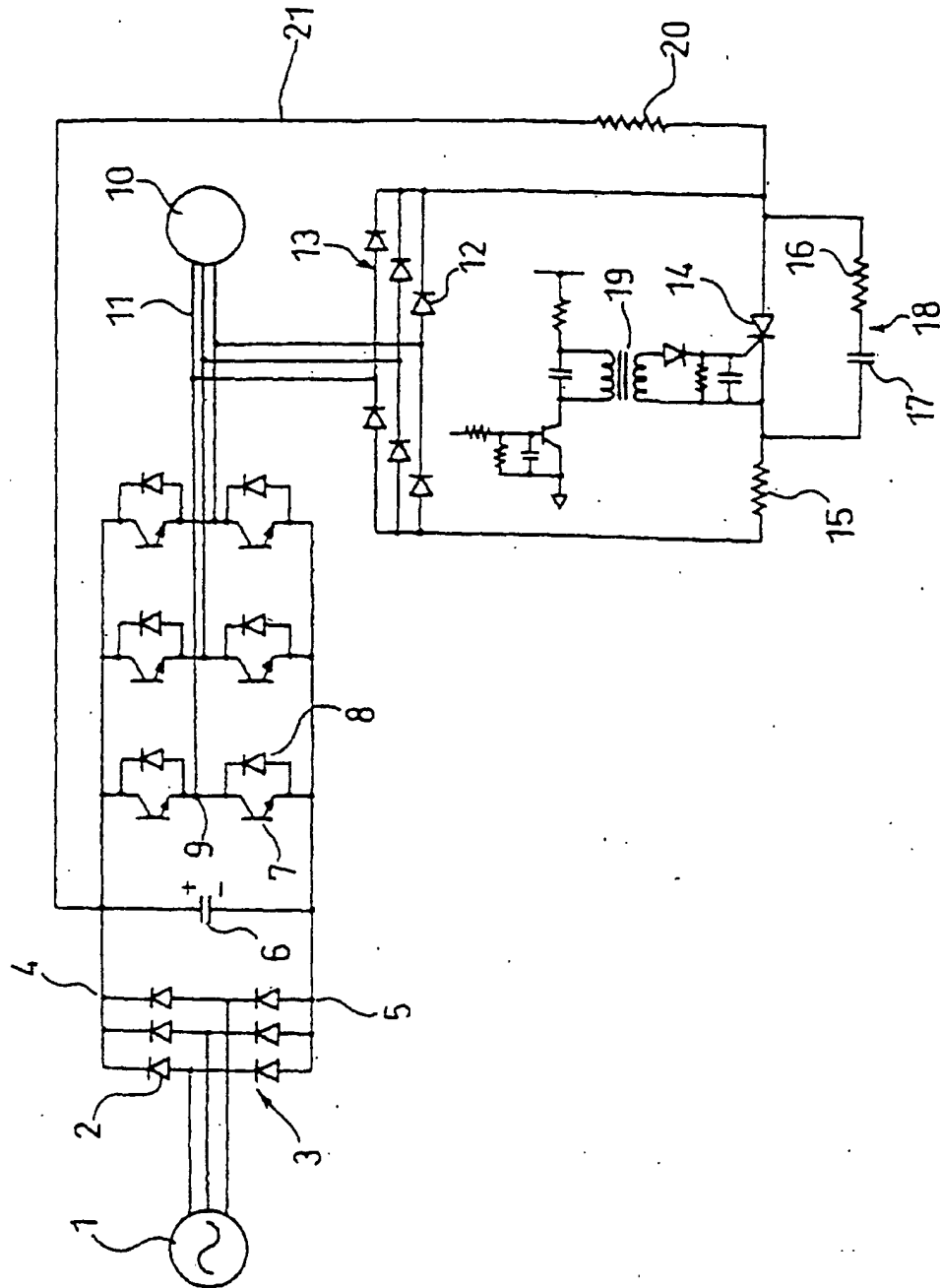


Fig. 2

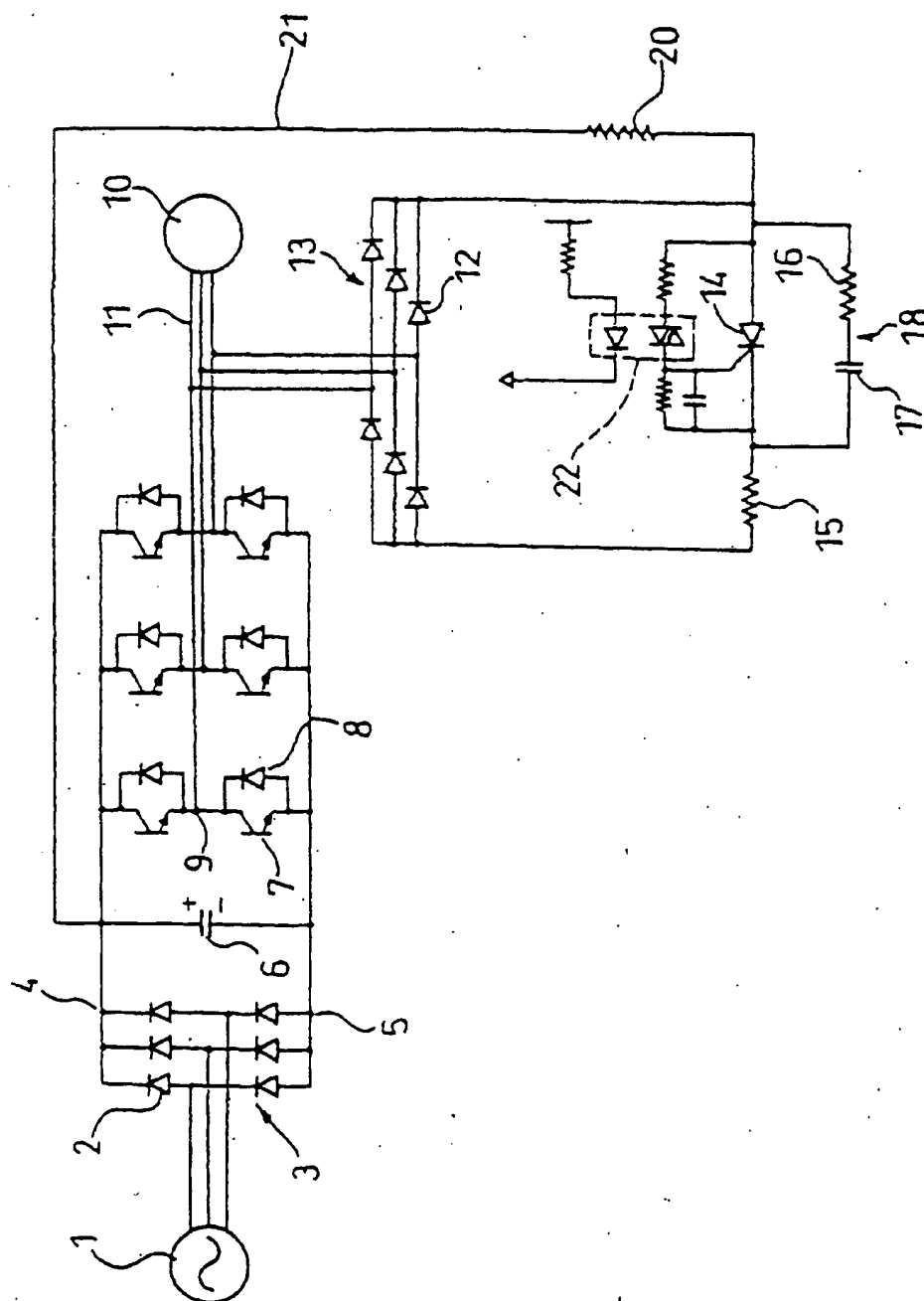


Fig. 3

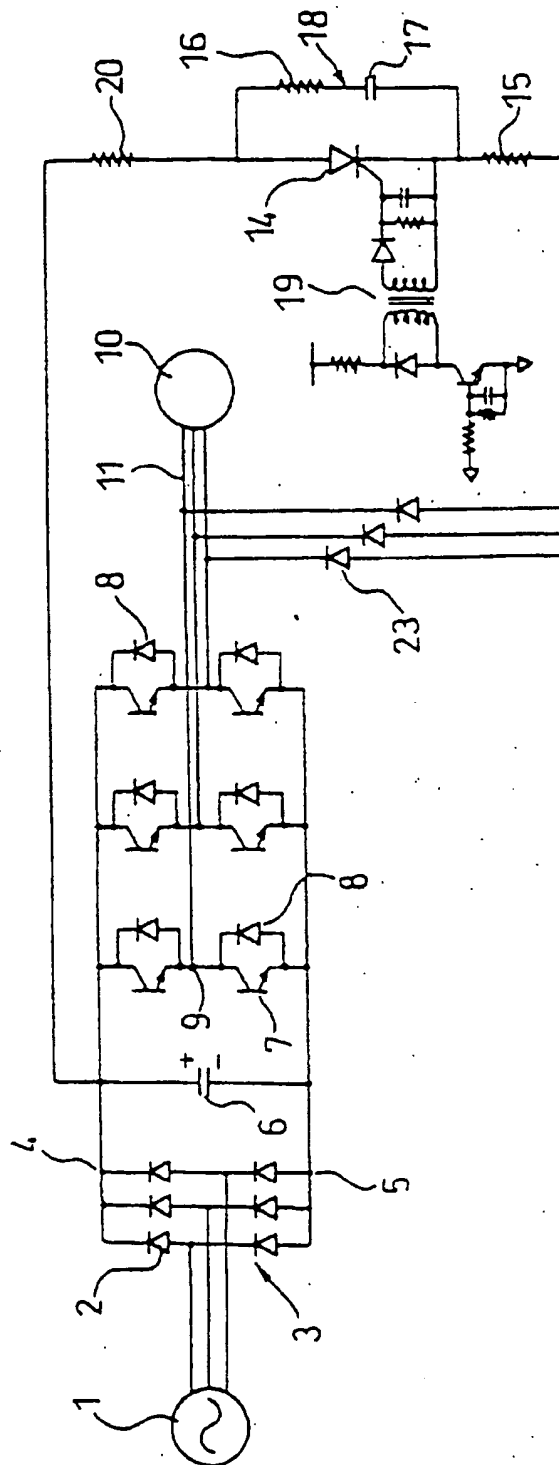


Fig. 4

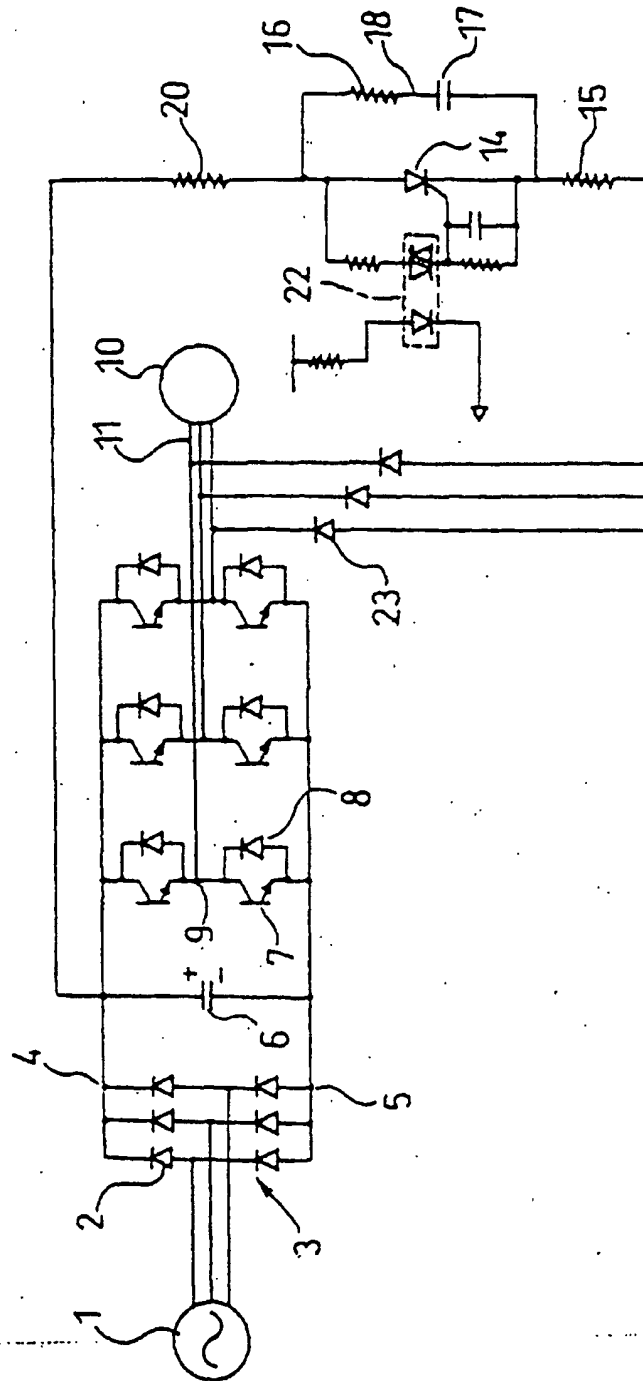
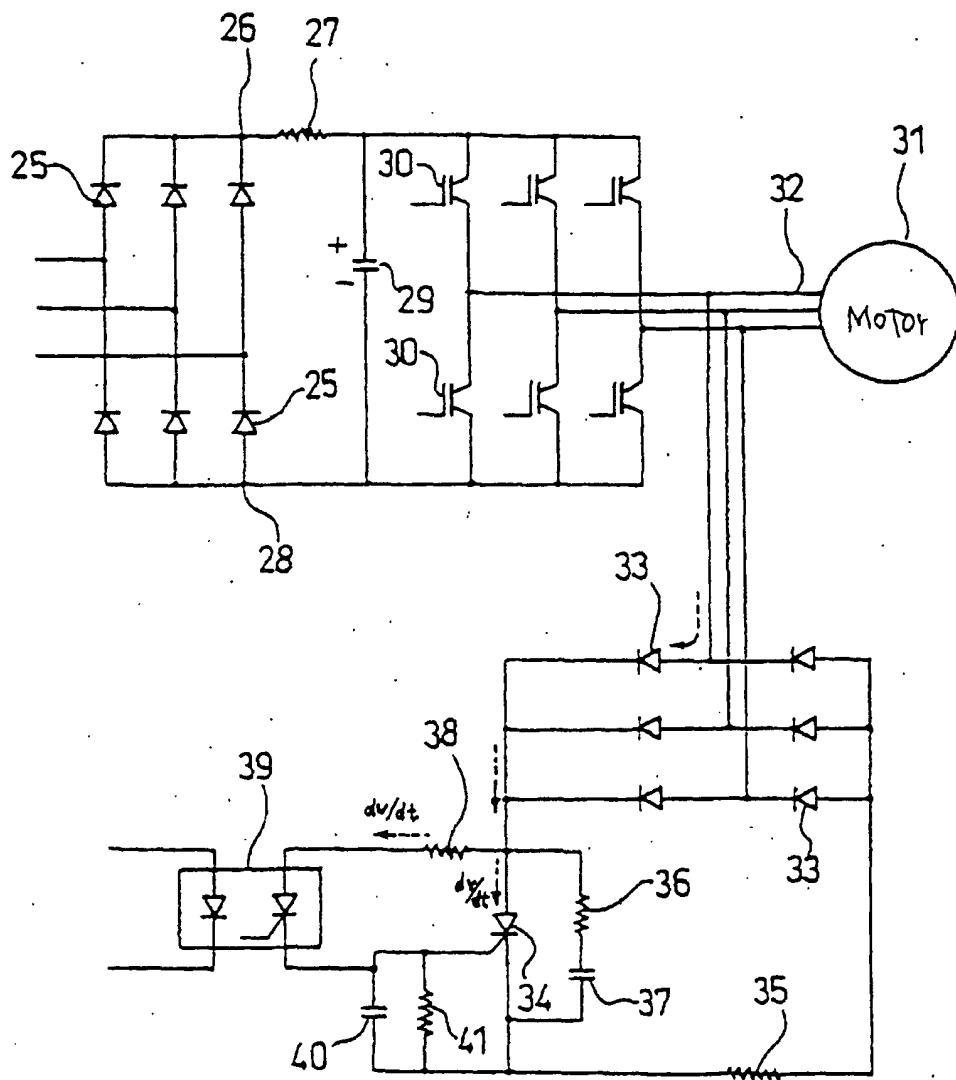


Fig. 5



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP00/01512

A. CLASSIFICATION OF SUBJECT MATTER
Int. Cl.⁷ H02P3/22

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
Int. Cl.⁷ H02P3/22Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
Jitsuyo Shinan Koho 1926-1995 Jitsuyo Shinan Toroku Koho 1996-2000
Kokai Jitsuyo Shinan Koho 1971-1994 Toroku Jitsuyo Shinan Koho 1994-2000

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	JP, 1-209973, A (SANKYO SEIKI MFG. CO., LTD.), 23 August, 1989 (23.08.89), page 2, upper left column, line 14 to upper right column, line 17 (Family: none)	1-5
Y	JP, 8-251961, A (Toyo Electric MFG Co., Ltd.), 27 September, 1996 (27.09.96), Claims (Family: none)	1-5

☐ Further documents are listed in the continuation of Box C.☐ See patent family annex.

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Date of the actual completion of the international search
19 June, 2000 (19.06.00)Date of mailing of the international search report
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